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APR 27 2005

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T-470 P003/004 F-069

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7550 02/11/2005

Greg J. Michelson  
MacPHERSON KWOK CHEN & HEID LLP  
Suite 226  
1762 Technology Drive  
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04/28/2005 EABUBAK2 00000026 502257 10629512

01 FC:1501 1400.00 DA

02 FC:1504 APPLICATION NO. 10629512

FILING DATE

FIRST NAMED INVENTOR

ATTORNEY DOCKET NO.

CONFIRMATION NO.

10/629,512

07/29/2003

Zheng (Jeff) Chen

M-15197 US

6913

TITLE OF INVENTION: MEMORY ACCESS VIA SERIAL MEMORY INTERFACE

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Tina Kavanagh

(Depositor's name)

Tina Kav

(Signature)

April 27, 2005

(Date)

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/11/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
CHO, JAMES HYONCHOL	2819		.326-041000		

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1 MacPherson Kwok Ch

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Lattice Semiconductor Corporation Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

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Authorized Signature

Greg J. Michelson

Date April 27, 2005

Typed or printed name

Greg J. Michelson

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44,940

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**MacPherson Kwok Chen & Heid LLP**

Date:	April 27, 2005		
To:	U.S. Patent and Trademark Office	Fax Telephone #:	703-746-4000
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From:	Greg J. Michelson	Date Sent:	
Subject:	Issue Fee for: Zheng (Jeff) Chen Serial No. 10/629,512 Filing Date: July 29, 2003	Time Sent:	
Client/File:	M-15197 US	Fax Operator:	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Zheng (Jeff) Chen; Fulong Zhang; Harold Scholz  
Assignee: Lattice Semiconductor Corporation  
Title: Memory Access Via Serial Memory  
Serial No.: 10/629,512 Filing Date: 07/29/2003  
Examiner: Unassigned Group Art Unit: 2816  
Docket No.: M-15197 US

Irvine, California  
April 27, 2005

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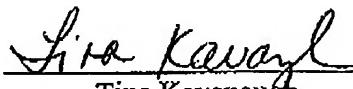
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